



Firmware Release Note

Prestige 645R-A1

Standard version

Release 3.40(GJ.2)C0

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Prestige P645R-A1 3.40(GJ.2)C0 Release Note

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Supported Platforms:

Samsung 9847+9850+8951

Versions:

ZyNOS Version: V3.40(GJ.2) | 5/23/2003 15:49:07

Bootbase Version: V1.07 | 5/21/2003 22:07:38

Notes:

1. The P645R-A1 (ADSL Router) is a small-office and home device that will allow a small LAN to access the Internet. By integrating ADSL, and NAT capability, P645R-A1 provides not only the ease of installation and Internet access, but also the most completed security solution to protect your intranet and efficient network management for data traffic. The P645R-A1 provides four single auto-sensing, auto-detection 10/100BASE-T Ethernet ports for connection to the user's local network, and a single RJ-11 port for connection to ADSL line.

Main features:

- ADSL

- Support Multi-Mode standard (ANSI T1.413, Issue 2 ; G.dmt(G.992.1); G.lite(G.992.2)).
- Auto-negotiating rate adaptation
- ADSL physical connection ATM AAL5 (ATM Adaptation Layer type 5)
- Support multi-protocol over AAL5 (RFC1483)
- Support PPP over ATM AAL5 (RFC2364)
- PPP over Ethernet support for DSL connection (RFC 2516)
- Support VC-based and LLC-based multiplexing
- F4/F5 OAM

- Hardware

- One 10/100M Ethernet UTP ports for LAN connection

- Router

- NAT (includes multi-to-multi NAT) / SUA
- Packet Filter
- VPN (IPSec, PPTP, & L2TP) pass through support
- Port forwarding
- Embedded Web Configuration
- SNMP v1 & v2c with MIB II

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2. Modem code version: SAMSUNG, DSP Version 109.030225

3. Modem Setting :

ADSL Standard	Multi
MinSNRMargin	6 db
TCM mode	ON
FEC R=2 mode	OFF
Tx Power attn	Normal db

Known Issues:

Features:

Modification in 3.40(GJ.2)b12 (May 23, 2003)

1. [FEATURE ENHANCE] Add the CI command for the request of TE to decrease the test period.
2. [BUG FIXED]
Symptom: The service provider default set to WWW.DDNS.ORG not WWW.DynDNS.ORG. The default setting should be WWW.DynDNS.ORG.
Condition: In the SMT and Web Configurator..
Solution: Fix this bug by update the DDNS default setting.

Modification in 3.40(GJ.2)b11 (May 22, 2003)

3. Update S5N8950 ADSL Debug Information to version 4.01.
4. Performance enhanced (about 20% in throughput test) by decrease SDRAM delay timer.
Please also upgrade Bootbase to GJ1.07 when upgrade Ras to 3.40(GJ.2)b11.

Modification in 3.40(GJ.2)b10 (May 12, 2003)

1. Change the default setting to ENET ENCAP from PPPoE.
2. Change ARM MAC Rx/Tx buffer descriptor number to 64 from 60.
3. Change ARM MAC Rx/Tx buffer descriptor start address to 8-bit alignment.
4. [BUG FIXED] Merge the bug fixed from trunk 3.40.
5. [BUG FIXED]
Symptom: Press 13/14 in main menu, the system will accept under SMT & Telnet.
Condition:
Solution: Modify SMT Menu to fix this problem.

Modification in 3.40(GJ.2)b9 (May 08, 2003)

1. Change the default setting to PPPoE from ENET ENCAP.

Modification in 3.40(GJ.2)b8 (February 28, 2003)

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1. [BUG FIXED]

Symptom: It's hard to link up with Siemens TI DSLAM in TianJin.

Condition: S5N8950 got some noise in the handshake state.

Solution: Update DSP code to 109.030225 to fix this problem.

Modification in 3.40(GJ.2)b4 (January 15, 2003)

1. [BUG FIXED]

Symptom: System can't receive the correct the VC through SAR

Condition: Happens only when configuring multiple PVC in bridge mode.

Solution: Fix SAR drive to keep upstream packet transmit smooth and alive.

2. [BUG FIXED]

Symptom: Upstream rate will not change if the link rate changed to higher from lower.

Condition: Happens only when link rate changed from lower to higher.

Solution: Fix SAR drive to clear Connections after link down.

3. [BUG FIXED]

Symptom: Upstream will stop if the AX4000 bi-direction stress test.

Condition: Happens only when configuring multiple PVC in bridge mode.

Solution: Add work-around function to SAR drive to reset Tx buffer if the SAR buffer is overflow.

4. [BUG FIXED]

Symptom: S5N8950 TC Upstream will stop if the AX4000 bi-direction stress test, but the S5N8947 SAR still works.

Condition: Happens only when configuring multiple PVC in bridge mode.

Solution: Add work-around function to SAR drive to reset S5N8950 TC register if the SAR buffer is overflow.

Modification in 3.40(GJ.1)b3

1. Change DSP code to version 109.151220.

Modification in 3.40(GJ.1)b2 (Nov 28, 2002)

1. Change DSP code to version 109.021203.