

Veristruct: An IEEE1364.1995 (Verilog 1995) Preprocessor

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1 Overview

Veristruct is an an IEEE1364.1995 preprocessor that adds some C-style struct support to the Verilog language. It takes as input Veristruct files (with a `.vs` extension) and struct definition files (with a `.struct`


```
struct_file := struct_block  
struct_block := struct_defn f struct_block g  
struct_defn :=
```

6 Changes

Changes since the 0.1 release³:

Signed support added (as per the request made by luked in RCHL10).

Bugfixes discussed in RCHL12.

7 Examples

7.1 Example .struct file

Here is an example .struct file:

```
1 /*****
2  *
3  * This file is a part of the Rachael SPARC project accessible at
4  */
```

```

42  /* block
43  block */
44  };
45
46  struct complex {
47      signed reall [7:0];
48      signed imag [7:0];
49  };

```

7.2 Example .vs file

And an example .vs file that uses those structs:

```

1  /*****
2   *
3   * This file is a part of the Rachael SPARC project accessible at
4   * https://www.rr53-82\(c3-82\(s3-75\(84-82\(c3-e2\(c3-l2\(c3-s2\(c3-p8\(53-82\(c3-r2\(c3-82\(\(r\)--28\(13-82\(sa832T

```

```
47 wire      memory_bus mb3 = mb2[1];
48
49 assign     db2.primary.req = 1'b1, db2.primary.address[31:28] = 4'h4;
50
```

7.3 Example .v file

