

A FPGA-based Soft Multiprocessor System for JPEG Compression

Sun Wei *Technical University Eindhoven, the Netherlands*
sunwei388@gmail.com

Abstract

Fig.1. Microblaze processor core block diagram

Similar to most of RISC processors, Microblaze

Hardware System



We thank Prof. Jef van Meerbergen for his guidance and reviewing of this paper. We thank Joris van Emden and Marcel Lauwerijssen for providing high quality JPEG encoder reference code and support afterwards. Prof. Henk Corporaal provided credit and F

r