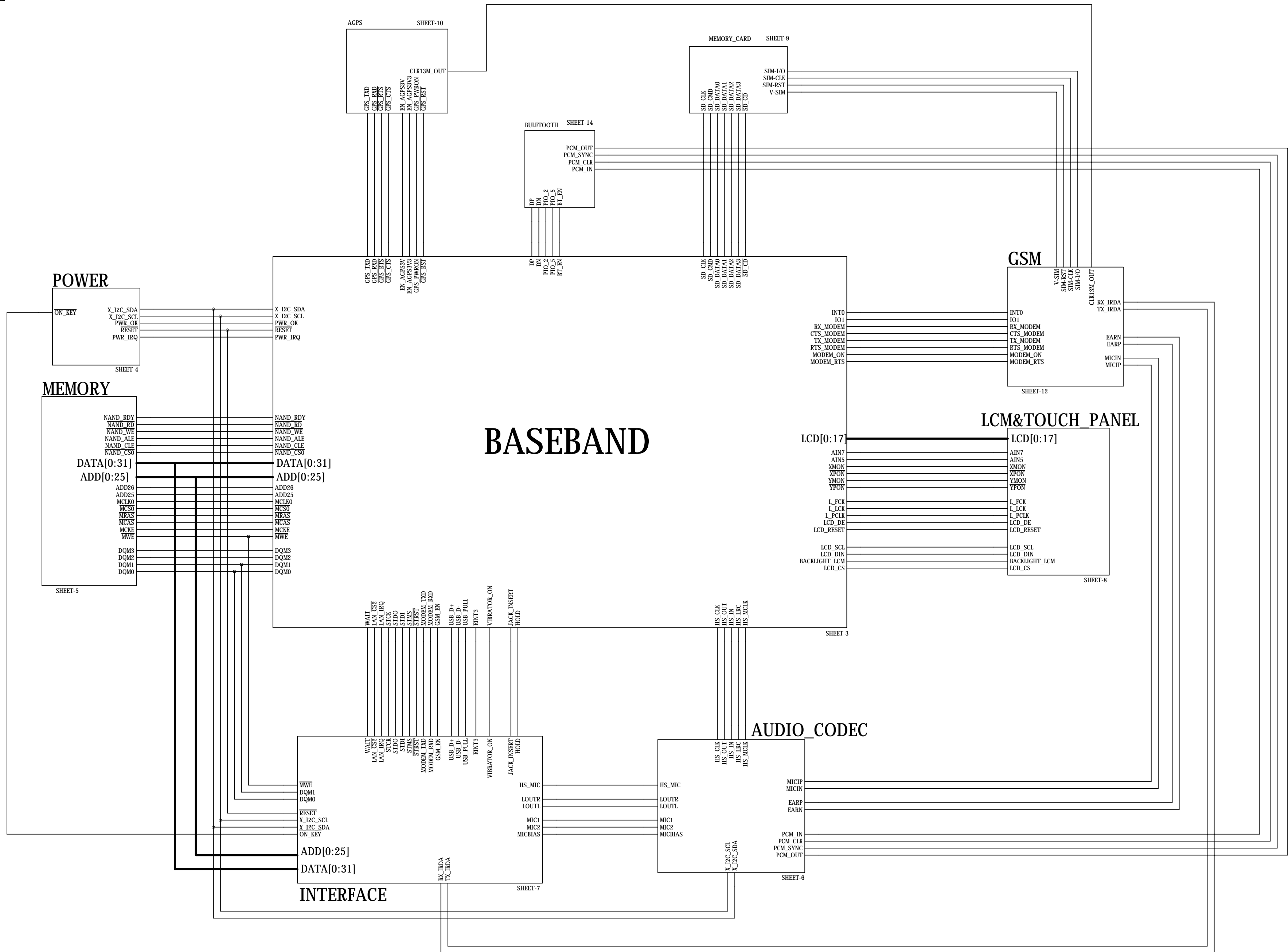


# Openmoko, Inc GTA01B-NEO1973 Schematics

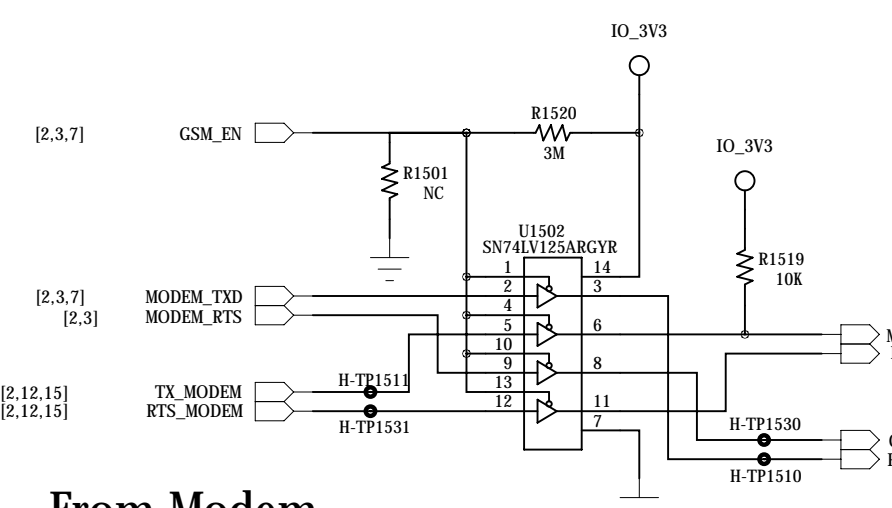
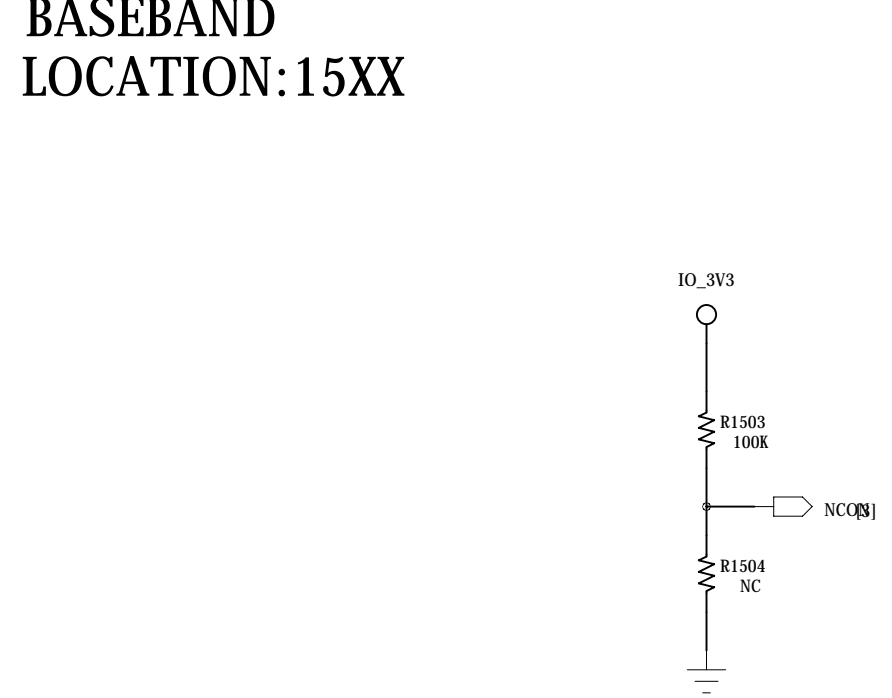
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or, send a letter to Creative Commons, 172 2nd Street, Suite 300, San Francisco, California, 94105, USA.

Version	Date	Signed off / Comment
GTA01BV4-RC0	2008-08-06	jOERG (ACK) / generic GTA01BV4 based, no cleanup, added component names, calypso GSM + Hammerhead GPS NDA
		schematics for board revisions prior to BV4 available on request: <a href="mailto:joerg@openmoko.org">joerg@openmoko.org</a>

# STRUCTURE



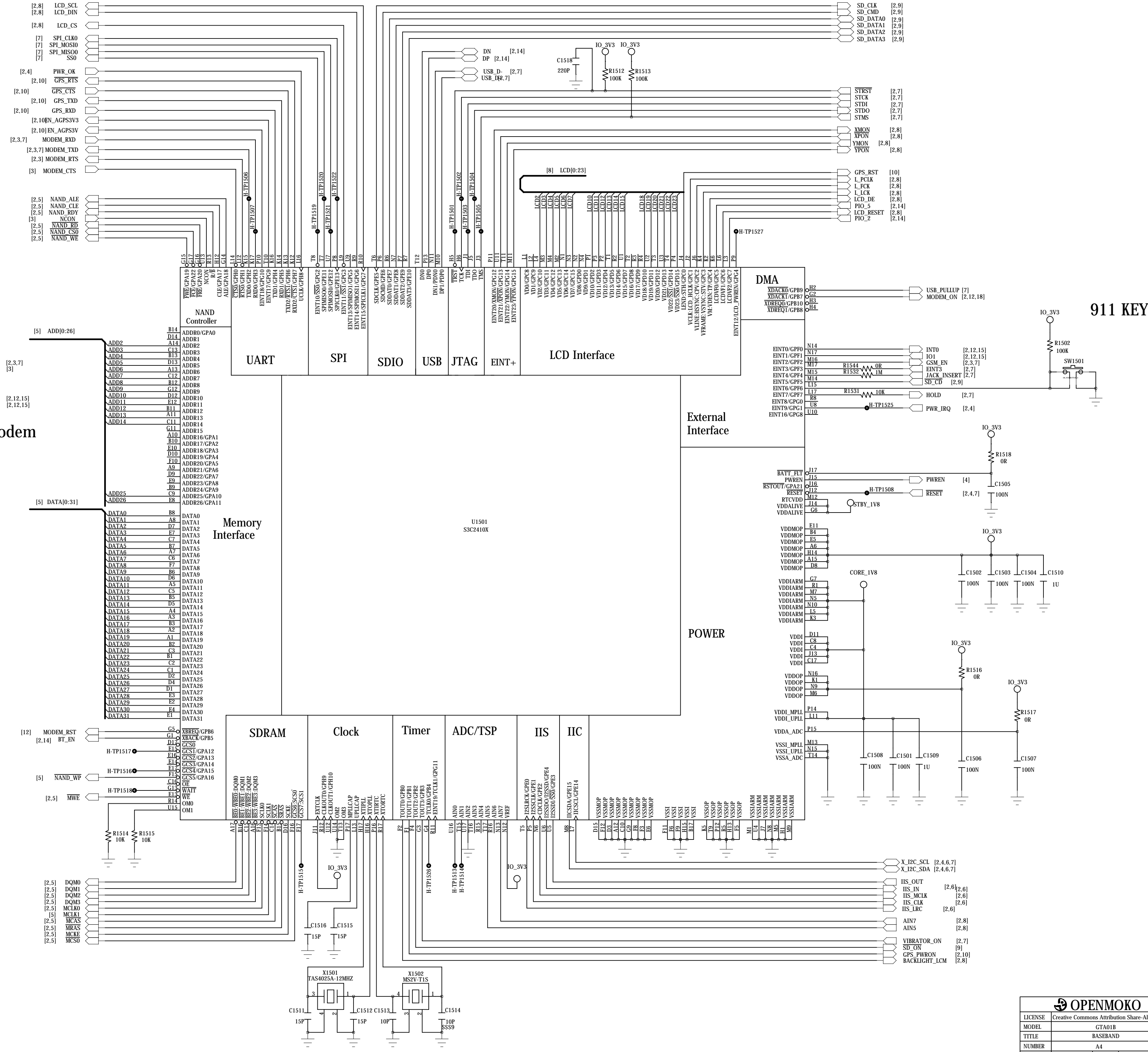
# BASEBAND LOCATION:15XX



From Modem  
From Modem

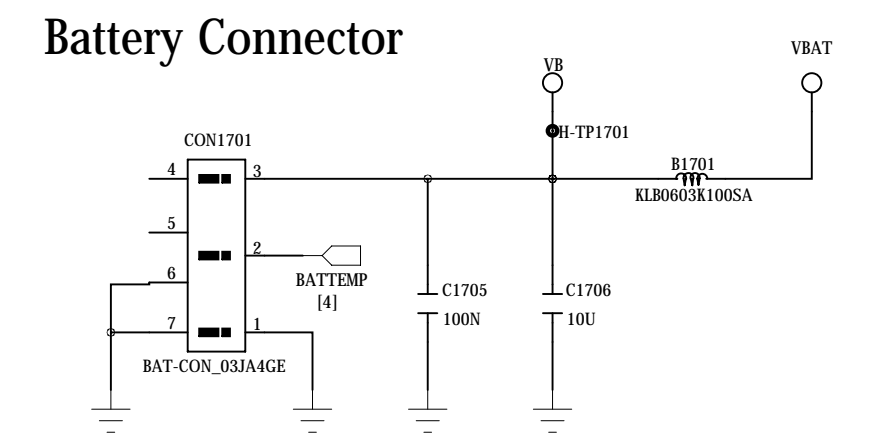
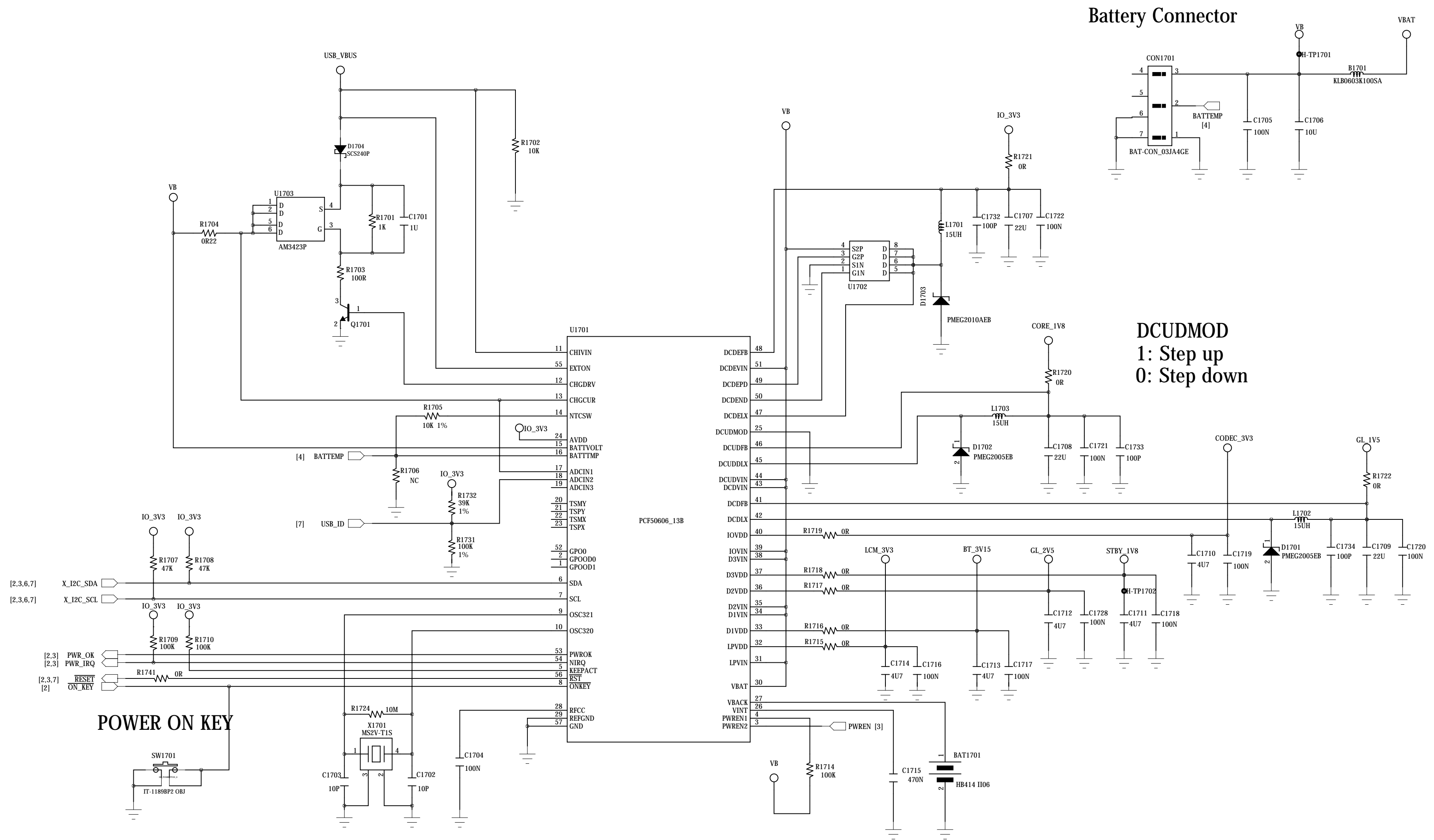
OM[1:0] : Clock Mode  
 00 : NAND Boot  
 01 : 16 Bit  
 10 : 32 Bit  
 11 : Test Mode

OM[3:2] : Clock Mode  
 00 : MPLL=Xtal, UPLL=Xtal  
 01 : MPPLL=Xtal, UPLL=EXTCLK  
 10 : MPPLL=EXTCLK, UPLL=Xtal  
 11 : MPPLL=EXTCLK, UPLL=EXTCLK



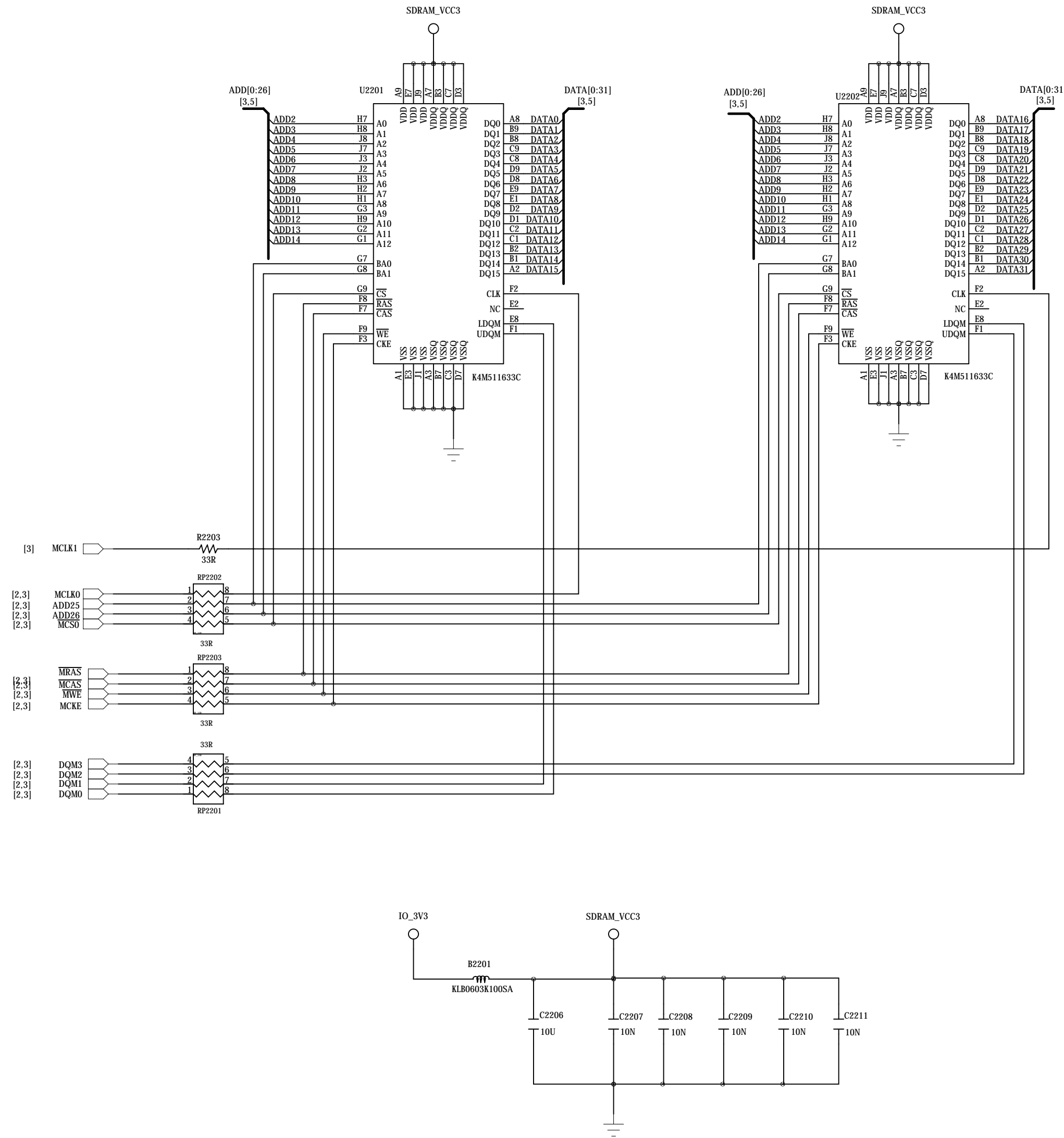
911 KEY

<b>OPENMOKO</b>	
LICENSE	Creative Commons Attribution Share-Alike 3.0
MODEL	GTA01B
TITLE	BASEBAND
NUMBER	A4
DWG.	Allen Chang
	SHEET 3
	DATE 2008/08/05

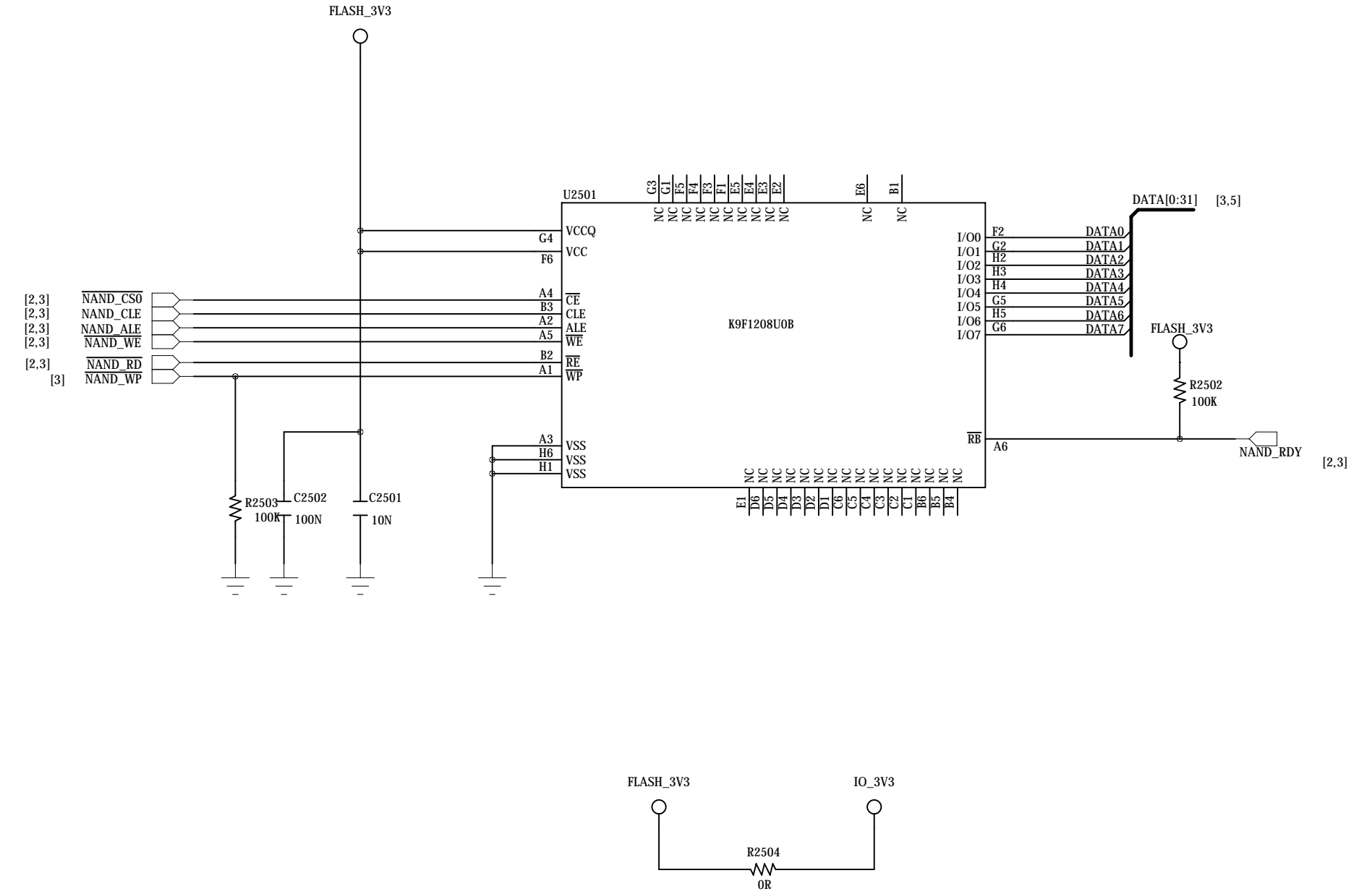


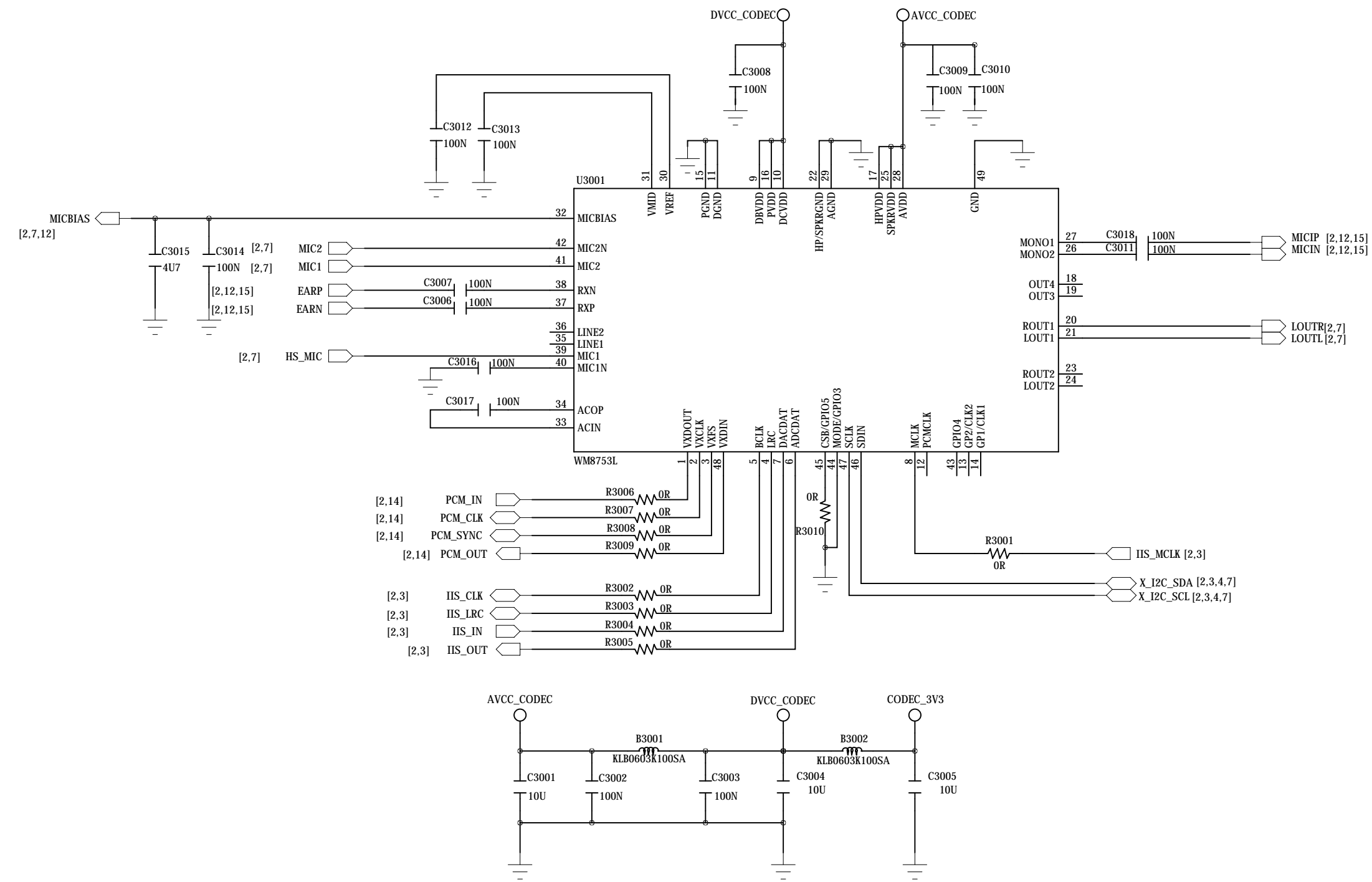
**DCUDMOD**  
1: Step up  
0: Step down

SDRAM\_ATLAS  
LOCATION:22XX



NAND\_ATLAS  
LOCATION:25XX



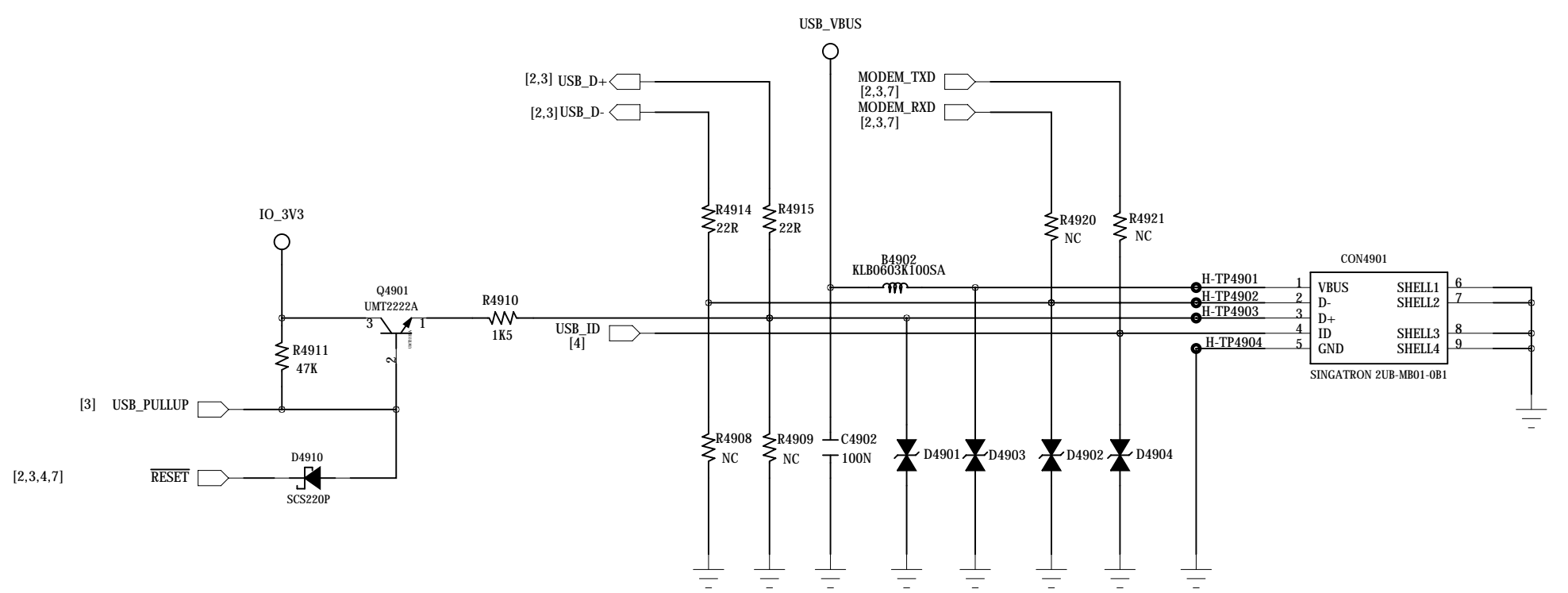


# USB LOCATION:49XX

## USB\_ID function

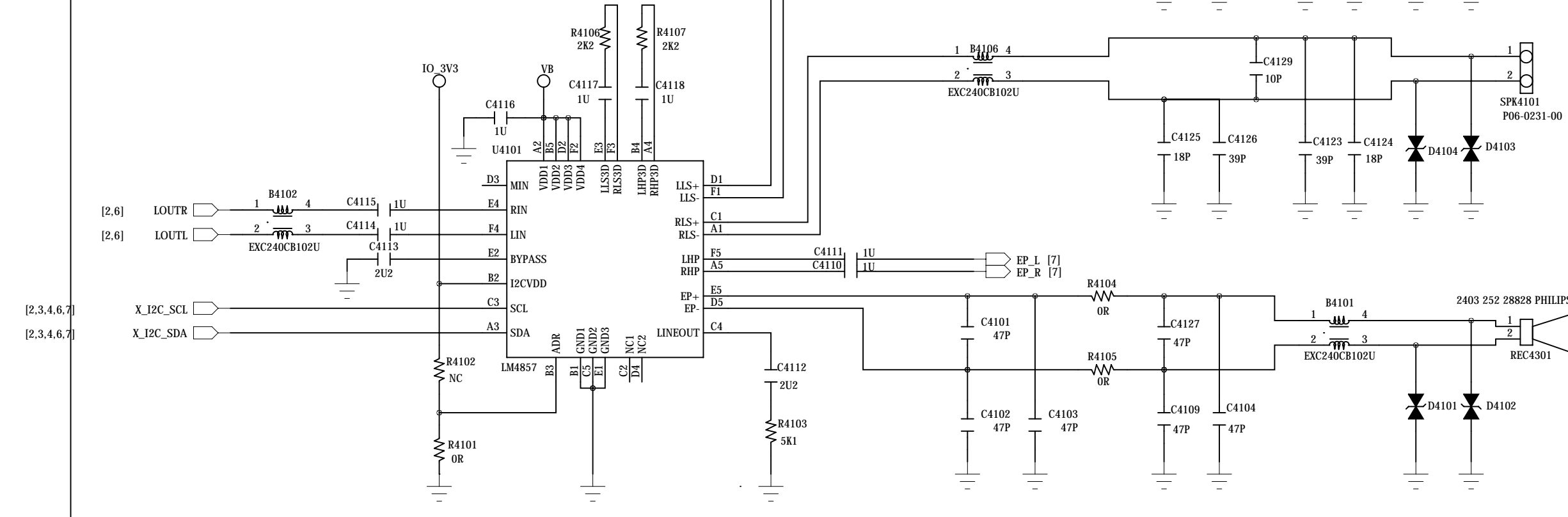
Assuming infinite ADCIN2 input resistance:

- 1) USB Device Mode: Rext > 100k  
=> Vacd will be > 1.85V (2.4V max)
- 2) USB Host Mode: Rext < 10  
=> Vacd will be < 330mV
- 3) Change Mode: Rext = 48k 1%  
=> Vacd will be = 1.5V

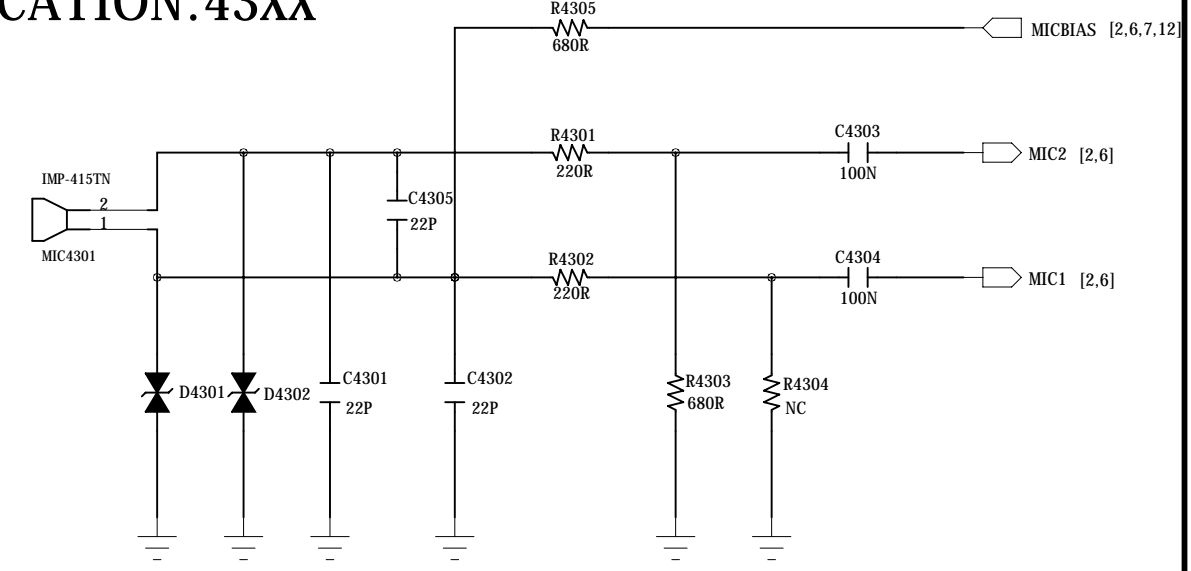


All varistors:  
KEV040209151A KARMAX

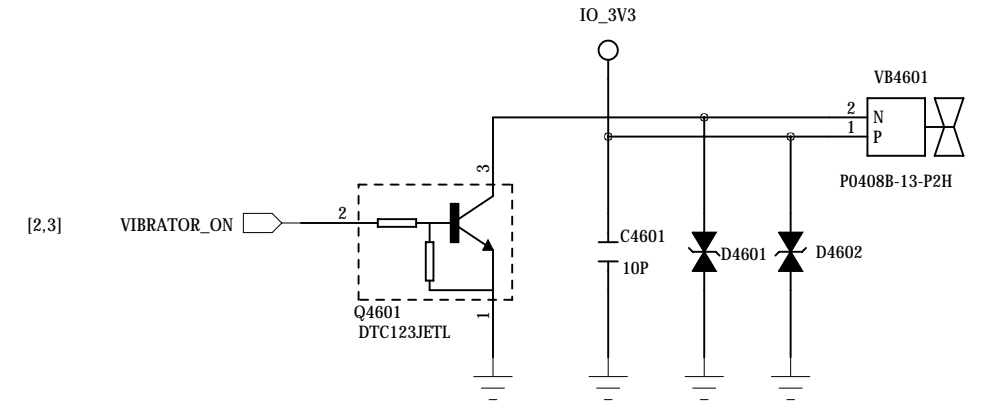
# SPK LOCATION:41XX



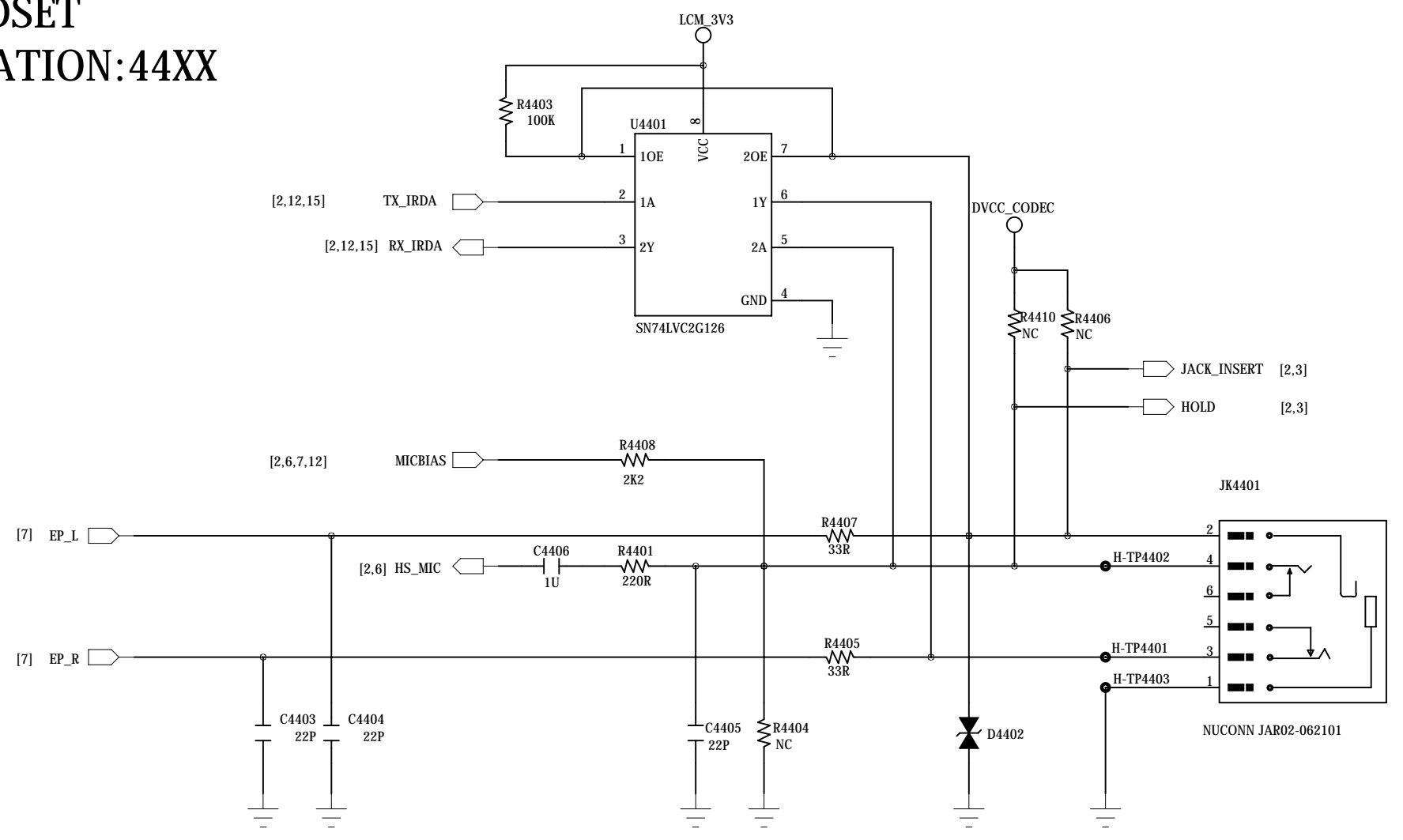
# MIC LOCATION:43XX



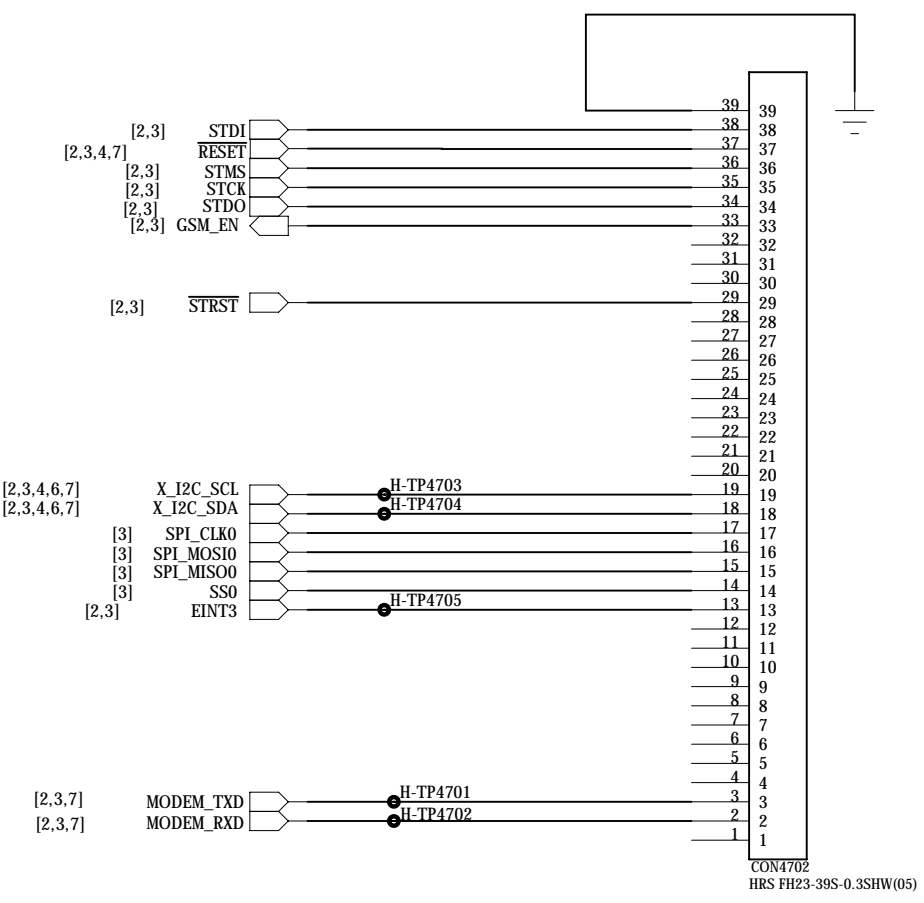
# VIBRATOR LOCATION:46XX



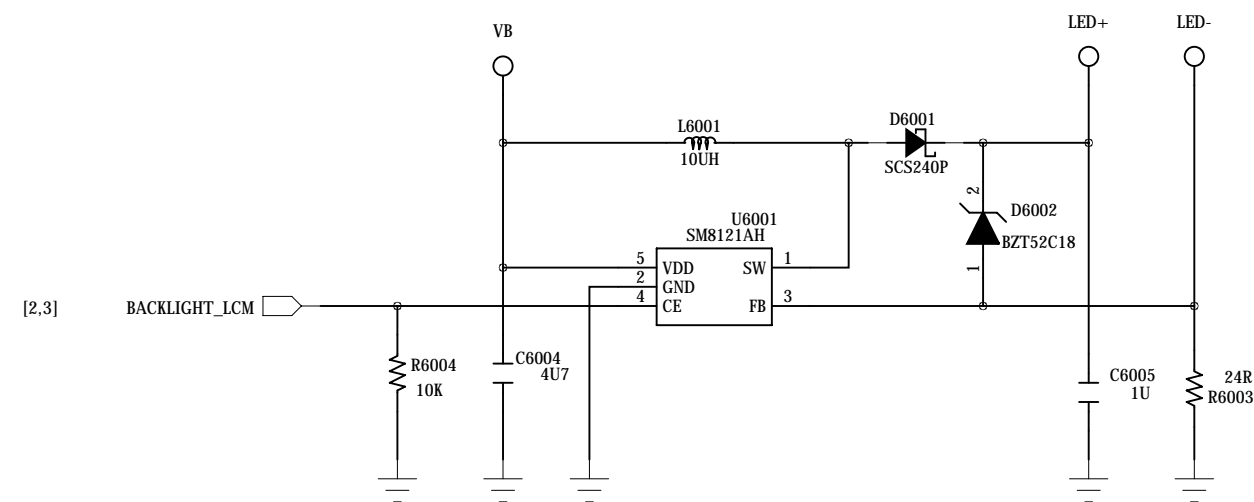
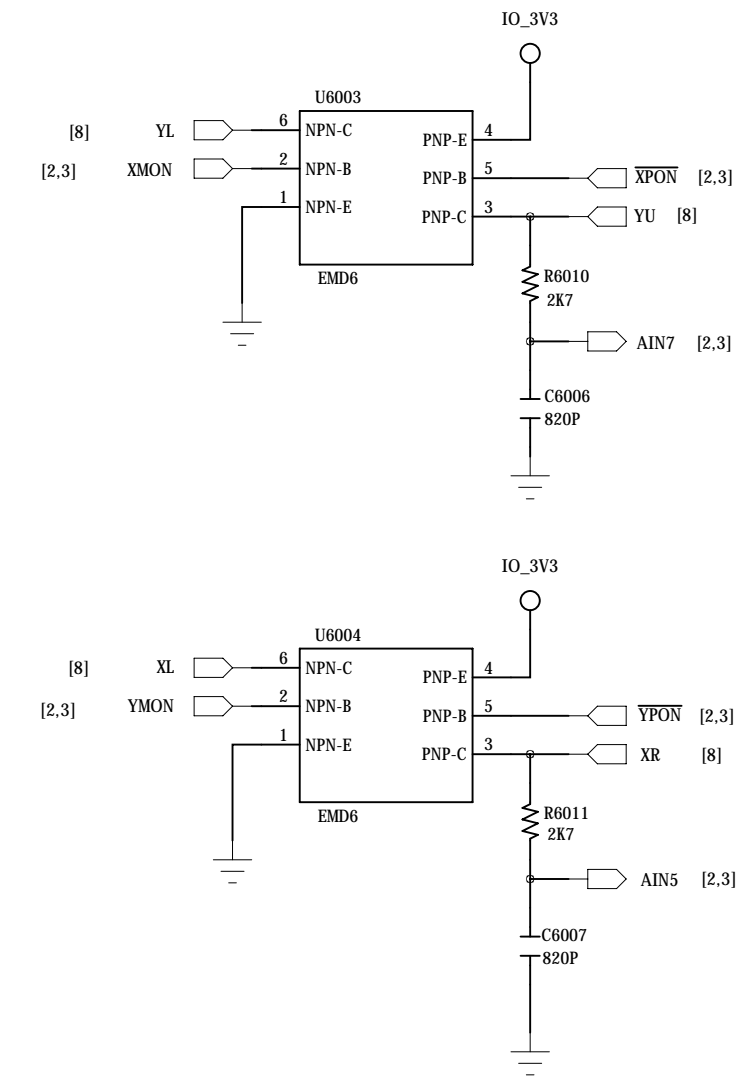
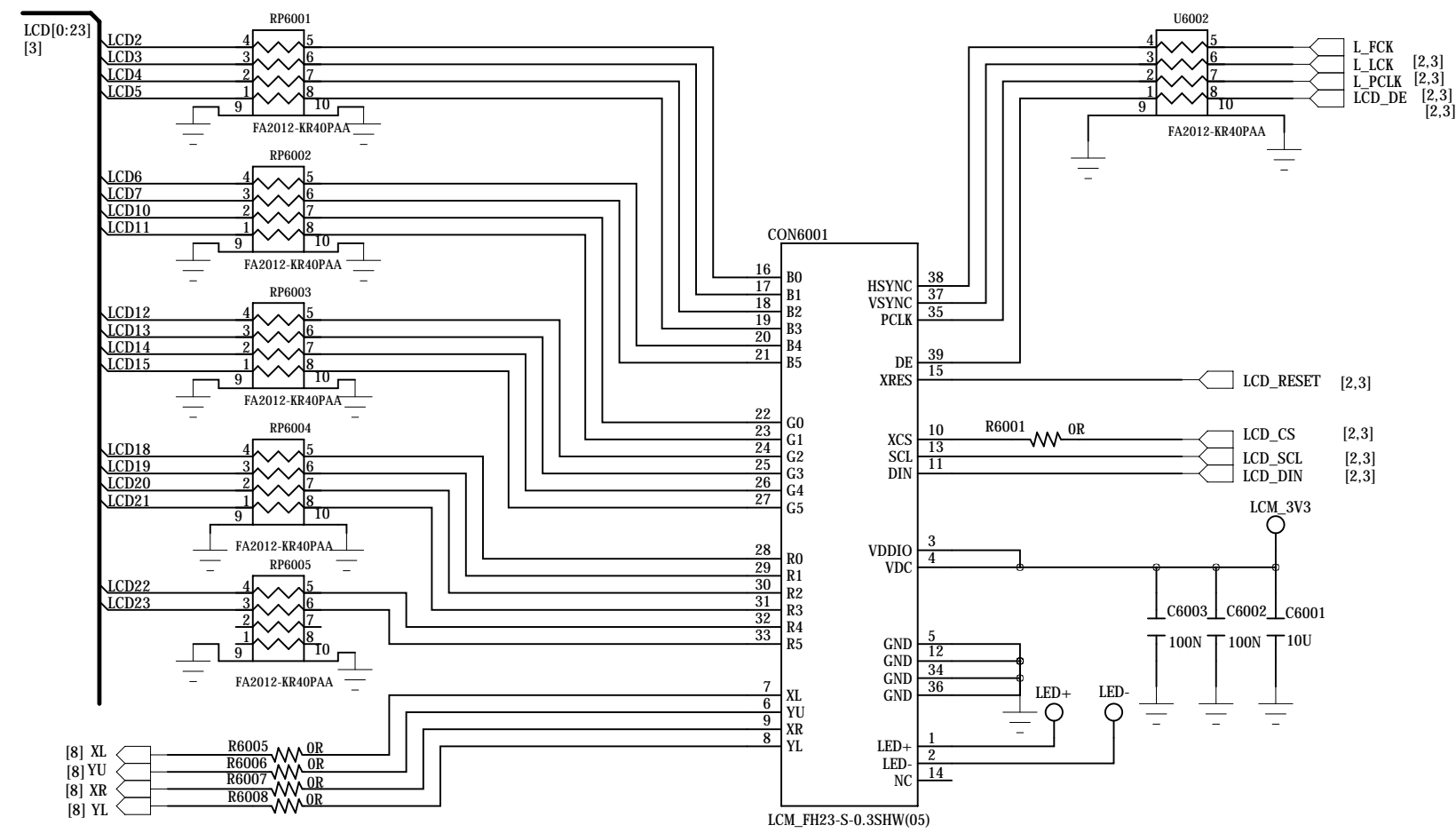
# HEADSET LOCATION:44XX



# DEBUG LOCATION:47XX

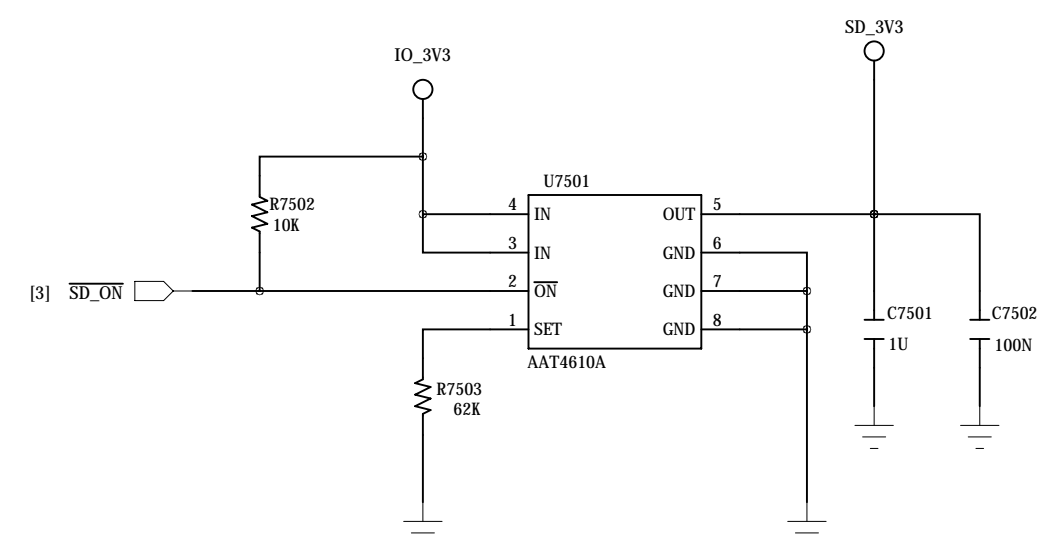
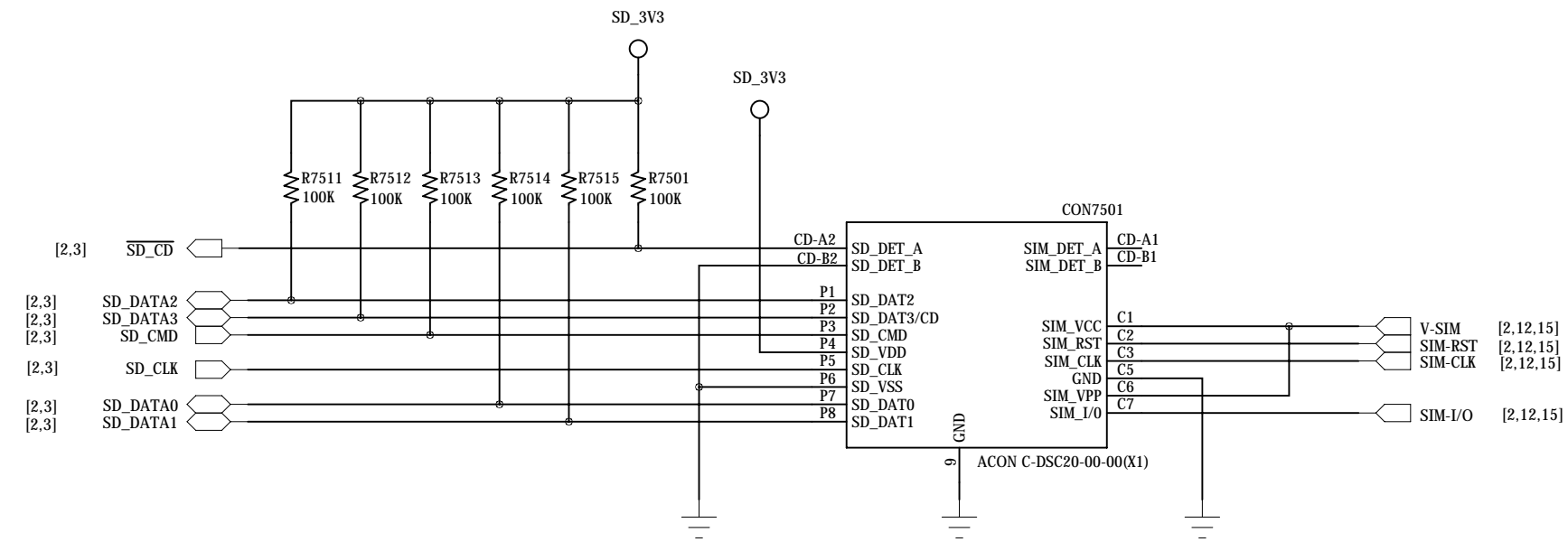


LCM\_TOUCH\_PANEL  
LOCATION:6XXX





MEMORY\_CARD  
LOCATION:75XX

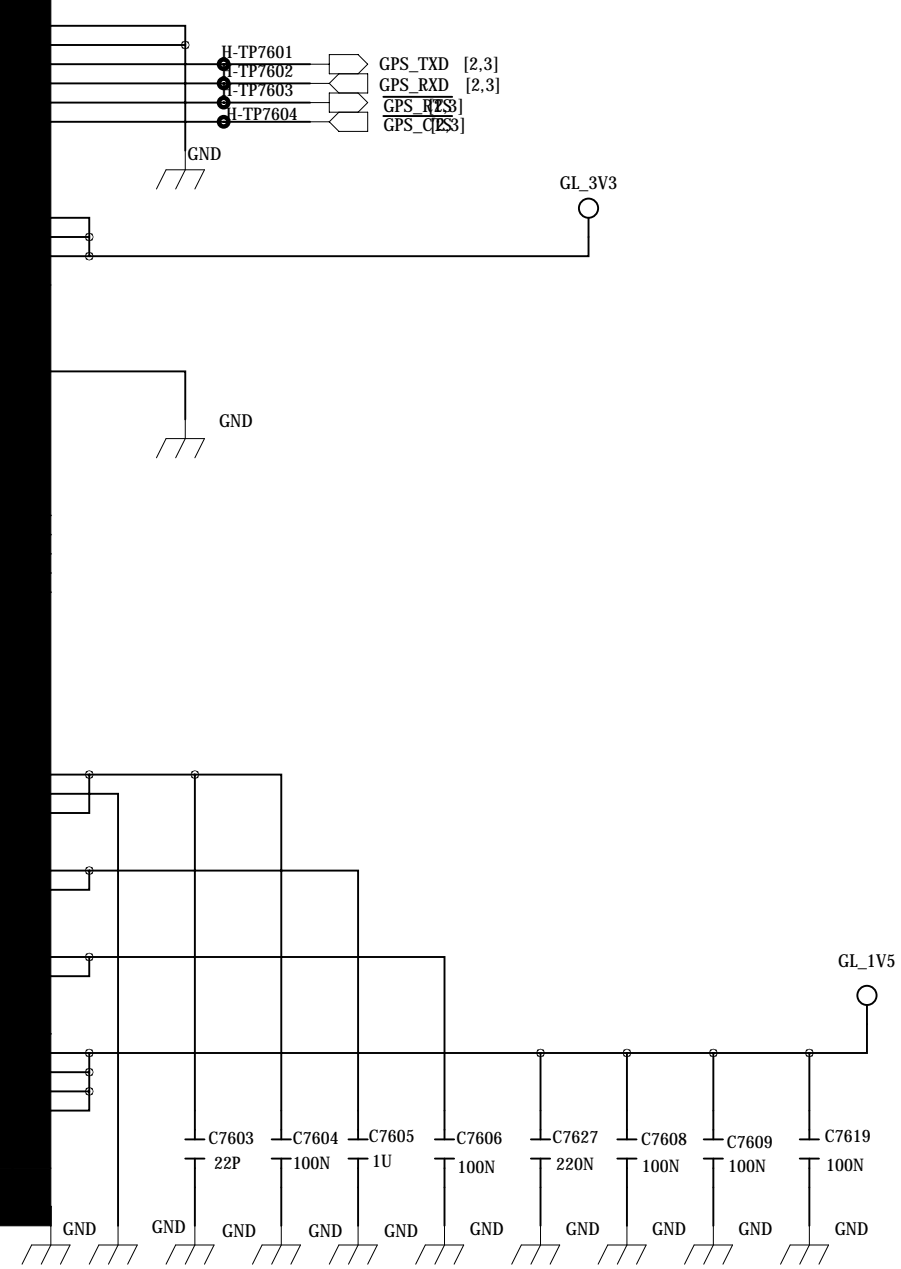
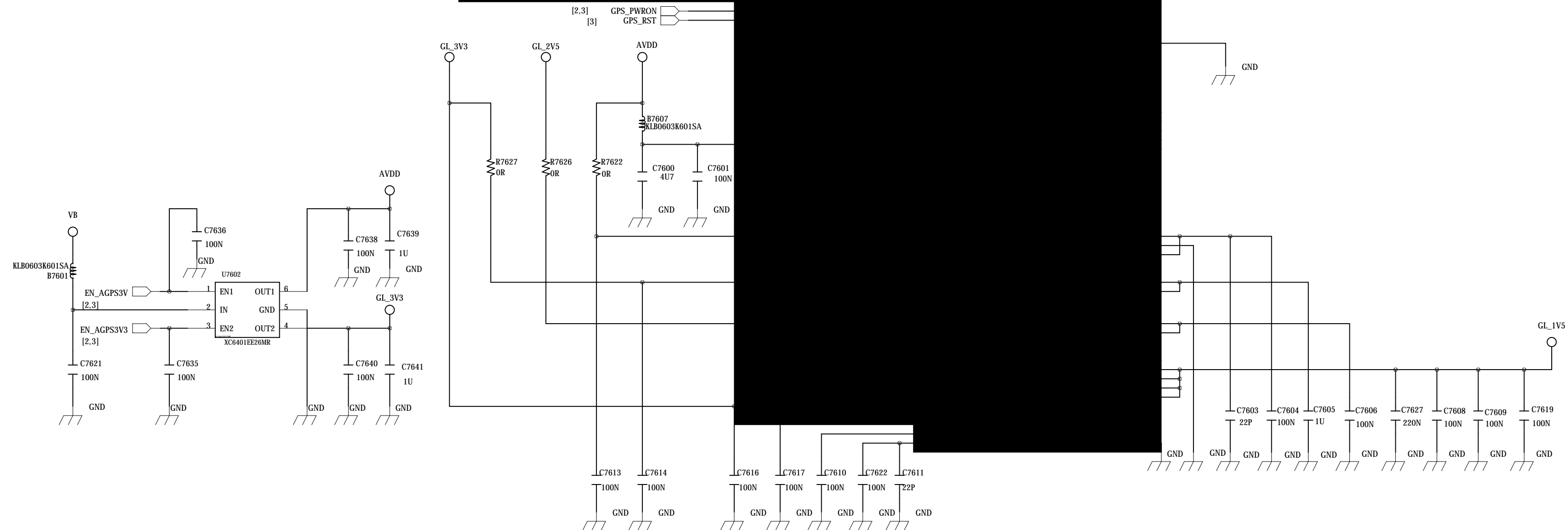
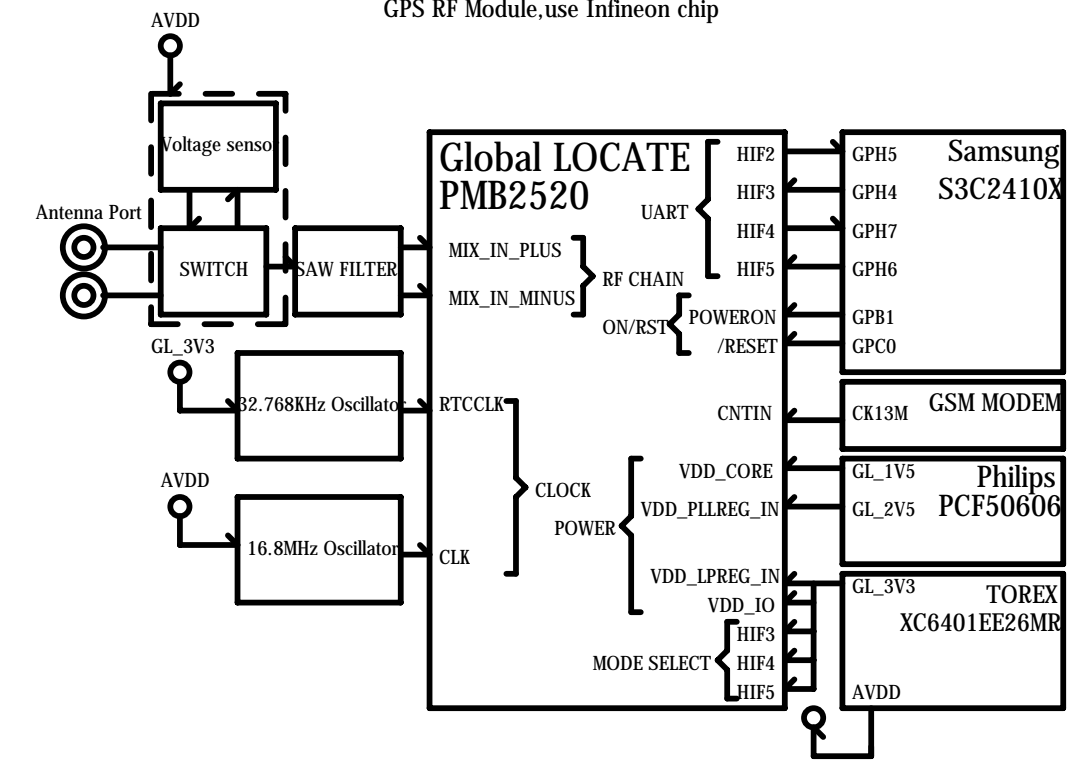
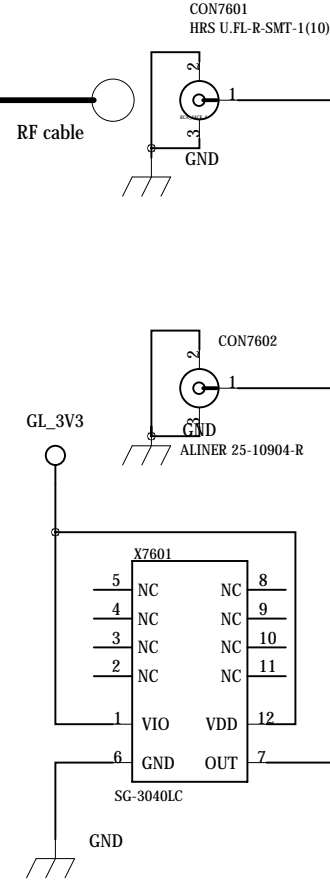
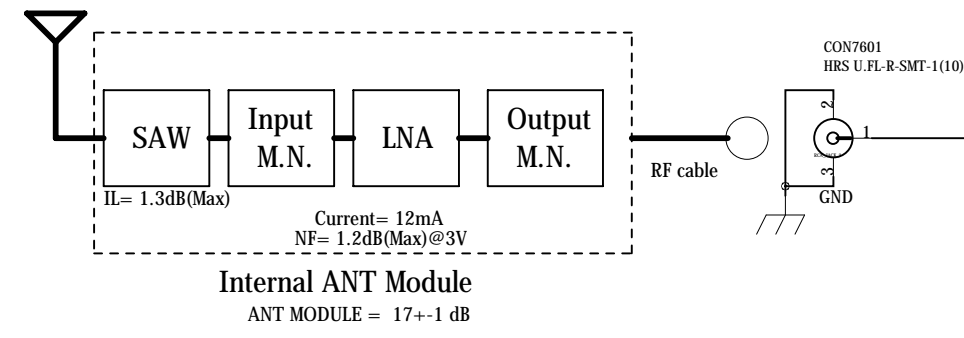


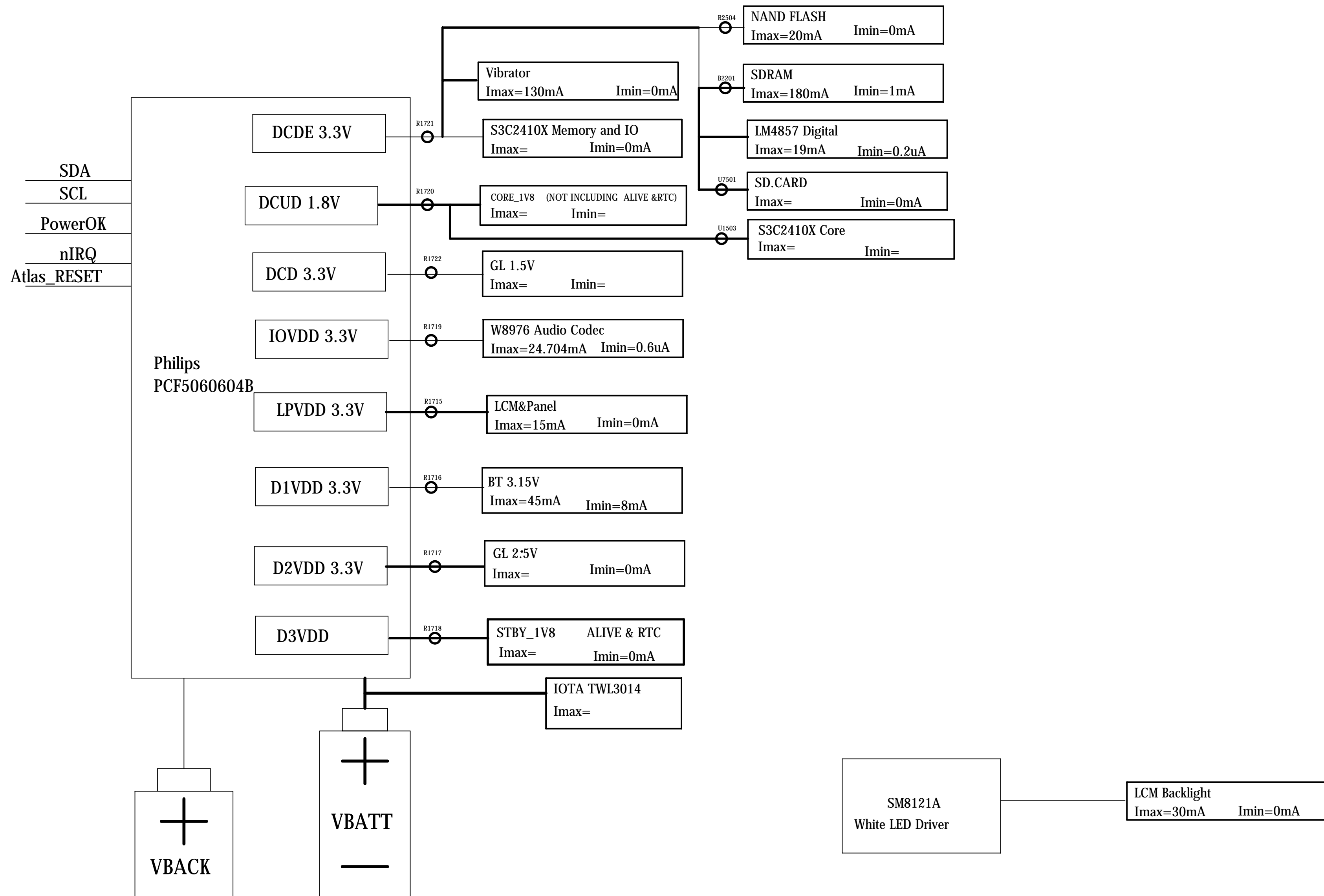
# NDA

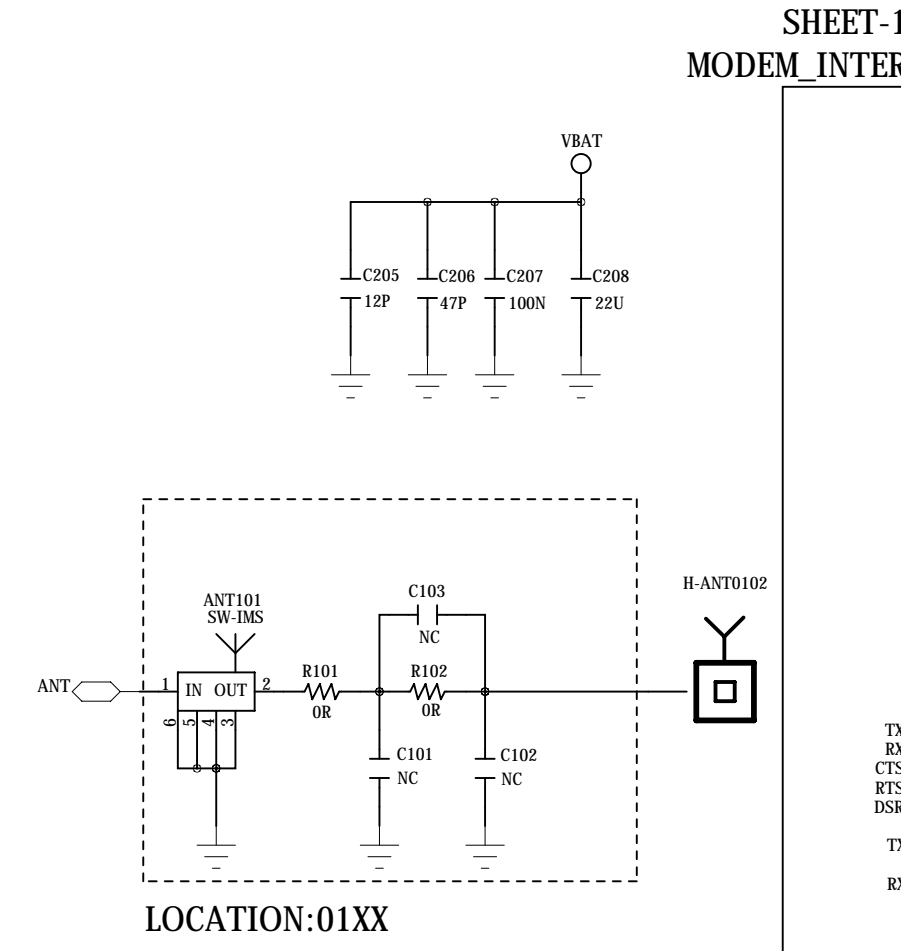
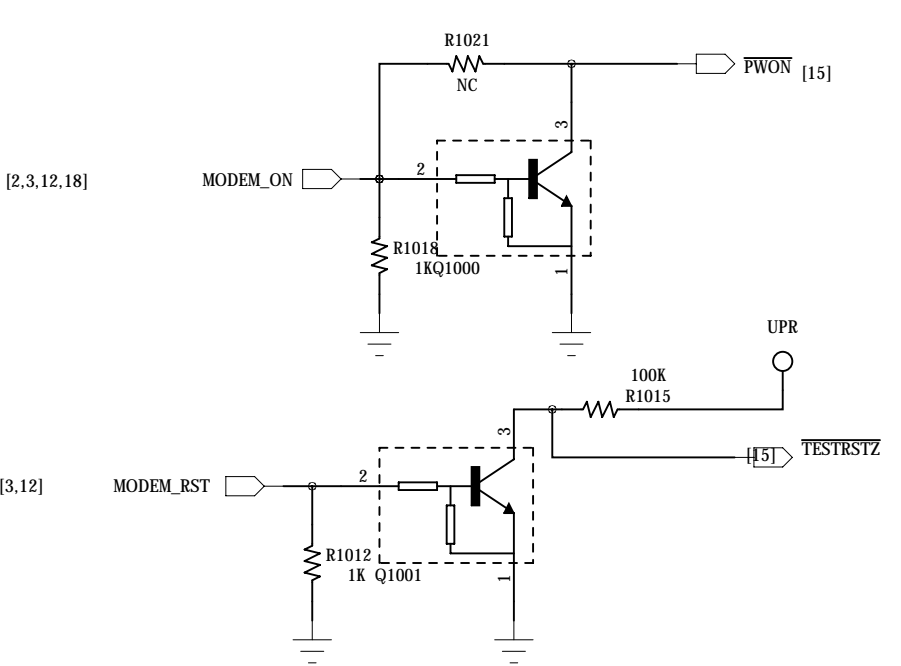
In fineon RX\_HAMMERHEAD OMB2520-V01

On Main Board design

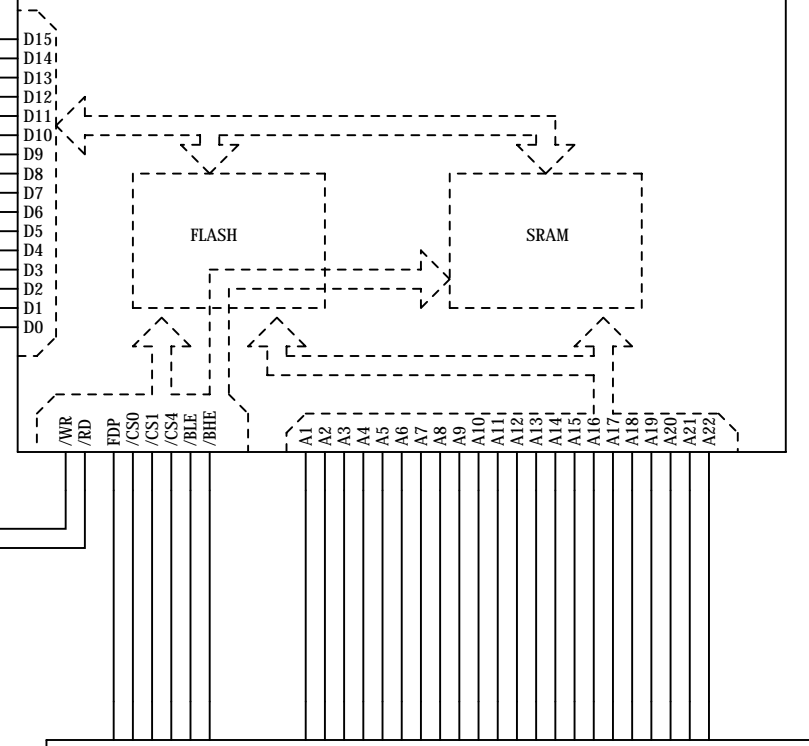
GPS RF Module, use Infineon chip



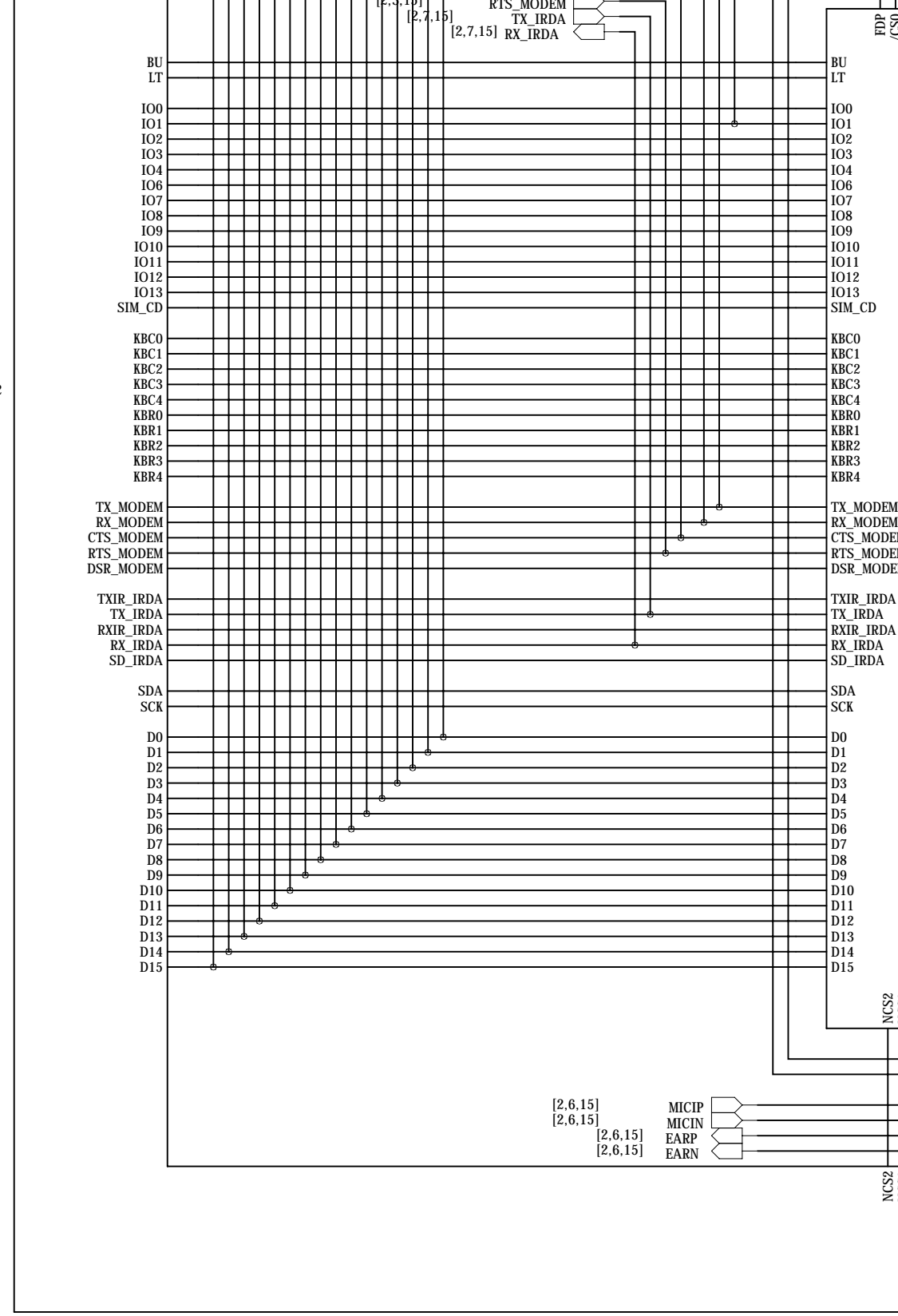




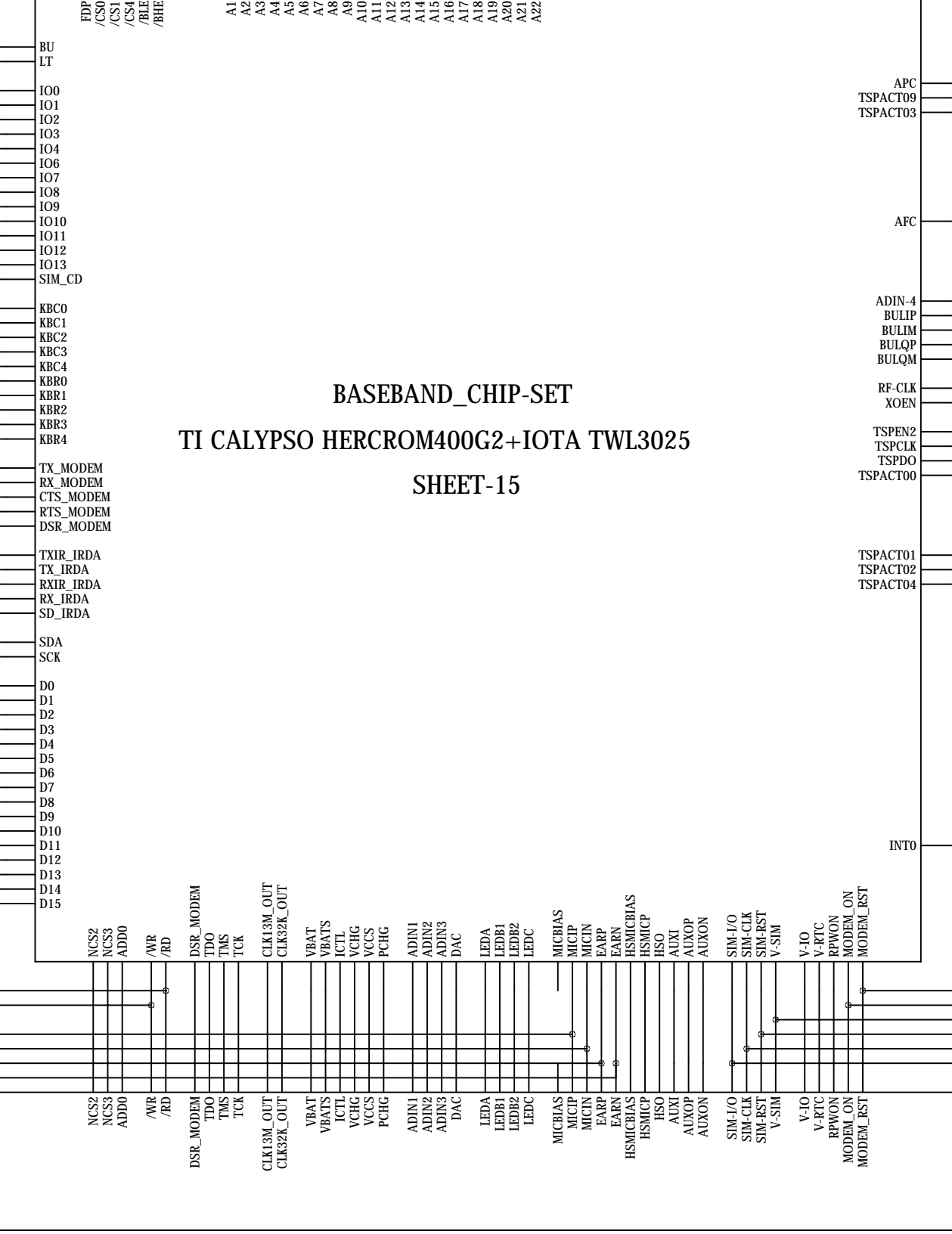
GSM\_MEMORY SHEET-17



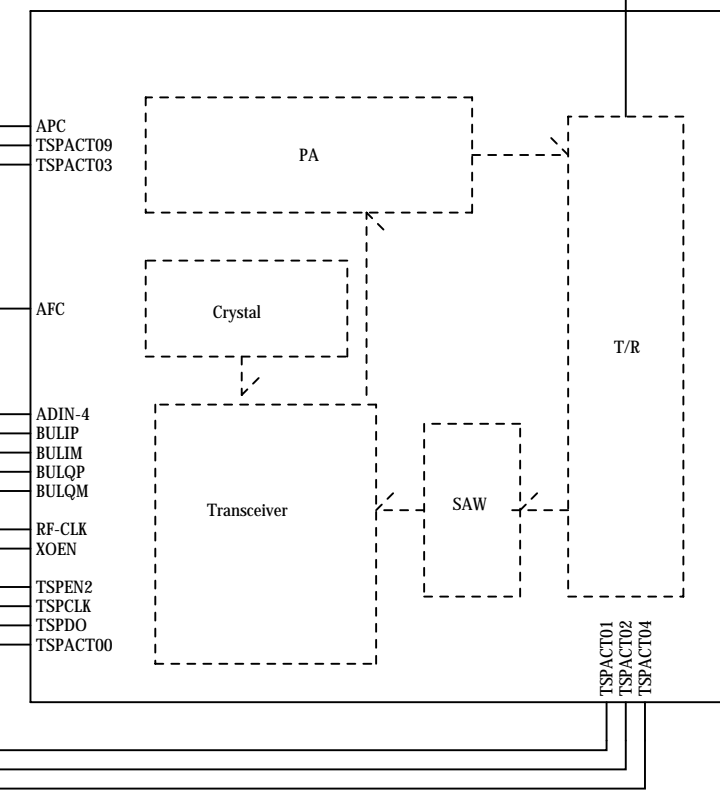
SHEET-18 MODEM\_INTERFACE



BASEBAND\_CHIP-SET  
TI CALYPSO HERCROM400G2+IOTA TWL3025  
SHEET-15

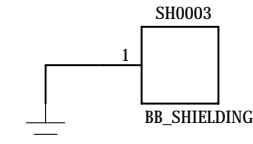
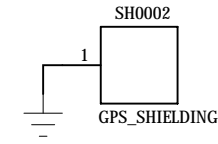
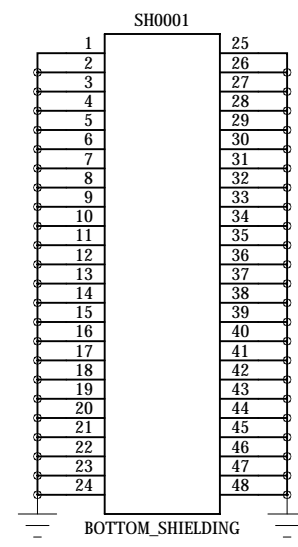


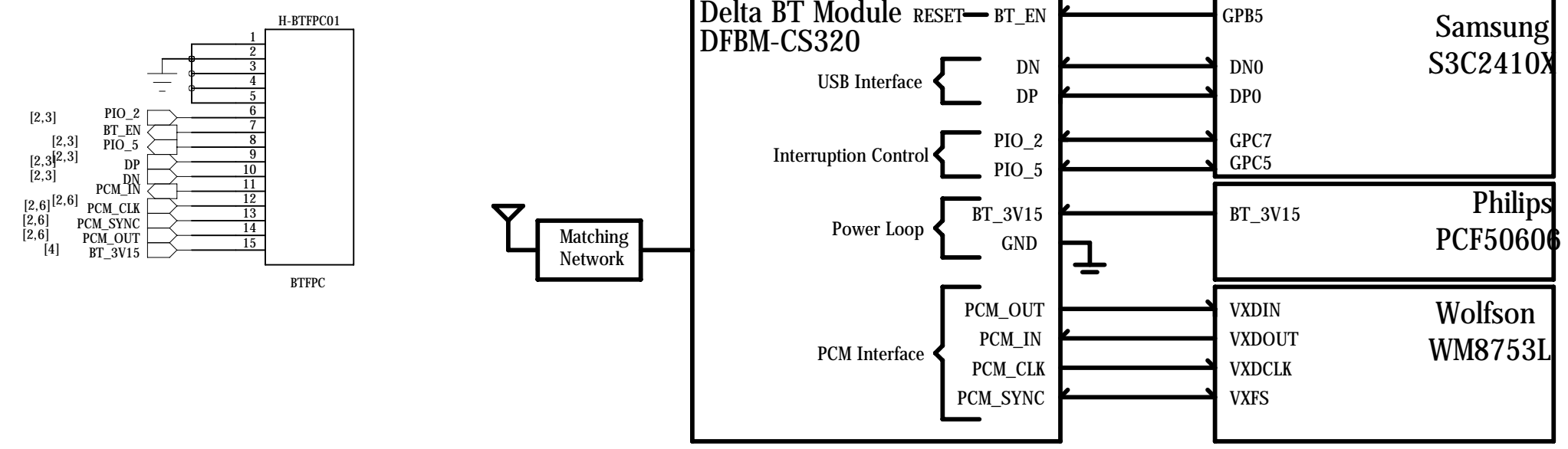
SHEET-16

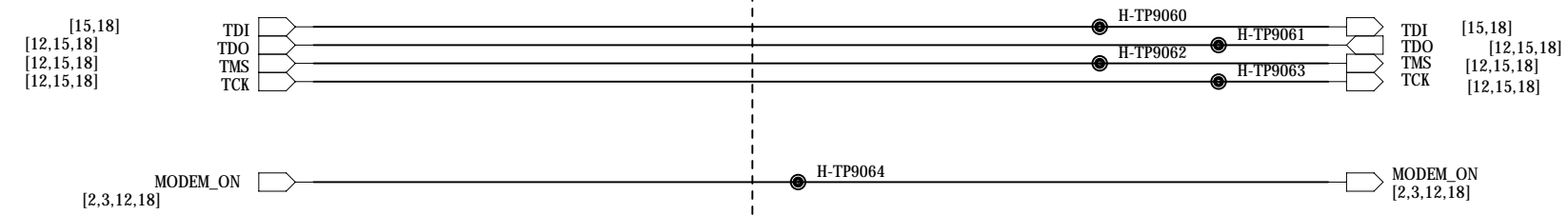



GSM\_RF

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TITLE	GSM
NUMBER	A4
DWG.	Shawn Lin
SHEET	12
DATE	2008/08/05







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MODEL	CTA01B		
TITLE	MODEM INTERFACE		
NUMBER	A4		
DWG.	Shawn Lin	SHEET	18
		DATE	2008/08/05